Docket No.AUS920030682US1

ABSTRACT OF THE DISCLOSURE

I^2C DEVICE INCLUDING BUS SWITCHES AND PROGRAMMABLE ADDRESS

An I^2C device is disclosed that includes a main I^2C section, bus switches, switch logic, and address logic as part of the I^2C device. The I^2C device is coupled to an I^2C bus for communicating with other I^2C devices and an I^2C bus controller that is also on the I^2C bus. The switch logic controls a current position of the switches. The I^2C device is coupled to the I^2C bus utilizing the switches. The switches control whether the main I^2C section, the address logic, the switch logic, or a combination of the main I^2C section, address logic, and switch logic is currently coupled to I^2C bus. The switches also can be used, if desired to remove from the buss all devices that are downstream from a given device containing switches. The address logic is used to receive and store the address of the I^2C device. The I^2C device will respond to the address that is stored in its address logic.